

1c978 U.S. PTO  
09/910915



Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

# U.S. UTILITY Patent Application

O.I.P.E. <i>AD</i> SCANNED <i>AC3</i> Q.A. <i>LA</i>	PATENT DATE
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APPLICATION NO. 09/910915	CONT/PRIOR F	CLASS 714	SUBCLASS 763	ART UNIT 2133	EXAMINER <i>Deed</i>
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APPLICANTS

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*Abraham  
Essau*

TITLE

Semiconductor memory device having ECC type error recovery circuit

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
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